

# 专业致力于 ICT在线测试仪

Q518D



## Features

- Component level MDA test and circuit level ATE test can do in the same platform at the same time to save the man hour in test station.
- Cost effective :integrated MDA module and function module at any time you want to realize your complicated test work.
- Both MDA and ATE automatic switch, additional fixture isolation does not require.
- Security :your already exist function module can integrated as a partial of this system through software or hardware linking. You need not to tell us the detail technique about you products.
- Flexibility: you can bundle any test module from current system easily upon you products innovation. You can update your system just in time.
- Nature graphic simulation interface, so that you can debug your test program very easily.
- Oracle Data base architecture, users can program their special facilities/functions by their own without modify the system software.

## Function:

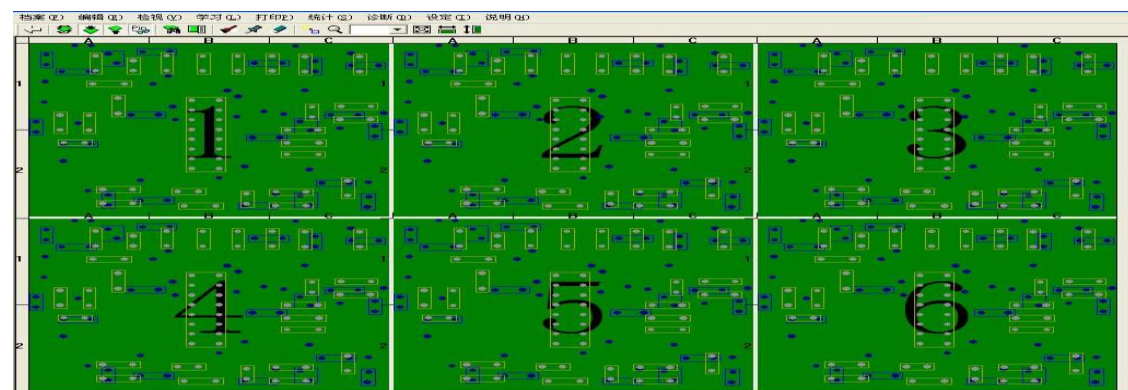
- 1.Data base architecture, the test results data write to data base to realize central data acquisition, analysis, remote monitoring facilities.
- 2.Supply a DC constant voltage constant current from a programmable power supply (PPS)to DUT to protect the damage through the test.
- 3.Supply AC220V/110V AC from AC source, voltage, frequency and trip protect is essential.
- 4.Dynamic test: test the voltage, current, waveform, frequency at the test point you assigned, and simulation the test activities through dry contact relay to realize the fully automatic test.
- 5.Support 14sets per card 0.5ms 1 Amp high speed dry contact relay card, maximum 42 sets contact relay to realize the manipulation of the test.
- 6.Support 10 sets per card maximum 30 sets 16A/250V high current dry relay contact facility to explain the additional power module for this system.
- 7.Flexible architecture: combine 3/6/11 wires measurement, you can choose the best solution for your test need
- 8.Digital input function:32 channels per switch card, maximum 256 channels digital input to monitoring the test data on the DUT.

## Console:

### 1. Edit screen

二端组件   三端组件   四端组件   IC开路																面板   全部步骤											
步号	E	零件名称	实际值	位置	量测 (+)	量测 (-)	模式	标准值	量测值	误差	延时	上限	下限	V	F <sub>g</sub>	F <sub>T</sub>	S	隔离1	隔离2	隔离3	隔离4	隔离5	隔离6	隔离7	隔离8	隔离9	隔离10
1	R1	1.00KΩ	30	21	R		1.00KΩ	000Ω		0	10	10		0.02	0	0	0	0	0	0	0	0	0	0	0	0	0
2	R2	10.0KΩ	21	32	R		10.0KΩ	000Ω		0	10	10		0.03	0	0	0	0	0	0	0	0	0	0	0	0	0
3	R3	100KΩ	34	21	R		100KΩ	000Ω		0	10	10		0.44	0	0	0	0	0	0	0	0	0	0	0	0	0
4	R4	47.0KΩ	35	21	R		47.0KΩ	000Ω		0	10	10		0.04	0	0	0	0	0	0	0	0	0	0	0	0	0
5	R5	470KΩ	36	18	R		470KΩ	000Ω		0	10	10		0.45	0	0	0	0	0	0	0	0	0	0	0	0	0
6	C1	10.0nF	12	32	C		10.0nF	000F		0	20	20		3.44	0	0	0	0	0	0	0	0	0	0	0	0	0
7	C2	100nF	25	64	C		100nF	000F		0	20	20		3.33	0	0	0	0	0	0	0	0	0	0	0	0	0
8	C3	1.00uF	26	34	C		1.00uF	000F		0	20	20		2.43	0	0	0	0	0	0	0	0	0	0	0	0	0
9	C4	10.0uF	21	42	C		10.0uF	000F		0	20	20		2.42	0	0	0	0	0	0	0	0	0	0	0	0	0
10	C5	100uF	39	41	C		100uF	000F		0	20	20		0.12	0	0	0	0	0	0	0	0	0	0	0	0	0
11	L1	47.0uH	67	56	L		47.0uH	000H		0	30	30		5.51	0	0	0	0	0	0	0	0	0	0	0	0	0
12	L2	47.0uH	24	89	L		47.0uH	000H		0	30	30		5.51	0	0	0	0	0	0	0	0	0	0	0	0	0
13	L3	100uH	35	87	L		100uH	000H		0	30	30		5.51	0	0	0	0	0	0	0	0	0	0	0	0	0
14	L4	100uH	64	84	L		100uH	000H		0	30	30		5.51	0	0	0	0	0	0	0	0	0	0	0	0	0
15	L5	1.00uH	34	54	L		1.00uH	000H		0	30	30		6.51	0	0	0	0	0	0	0	0	0	0	0	0	0
16	D1	700mV	12	64	D		700mV	000V		0	20	20		0.00	0	0	0	0	0	0	0	0	0	0	0	0	0
17	D2	700mV	9	95	D		700mV	000V		0	20	20		0.00	0	0	0	0	0	0	0	0	0	0	0	0	0
18	D3	700mV	4	24	D		700mV	000V		0	20	20		0.00	0	0	0	0	0	0	0	0	0	0	0	0	0
19	D4	700mV	6	26	D		700mV	000V		0	20	20		0.00	0	0	0	0	0	0	0	0	0	0	0	0	0
20	D5	700mV	3	31	D		700mV	000V		0	20	20		0.00	0	0	0	0	0	0	0	0	0	0	0	0	0

### 2. Plate diagram



Device configuration					
Test points		Standard Configuration	256 Channels	Maximum Points	1024
		Maximum Expanded Charge Points 65536 Customized			
Channel board	CMOS +Relay	128 channels per channel board			
Isolation points		10 per test step, automatic learning isolation			
Source	Conventional signal Source	DCV:±10mV~±7.5V OR 15V ACV:10mVpp~10Vpp ACDVI:10u~20mA			
	Test frequency	100、1000、10K、100KHz、2MHz、(50Hz)			
Test step		No limit			
Test frequency		component test: 1ms~40ms O/S test: 0.5 s /102			
Test scope	Resistance	0.052~100MΩ (2-wire type) 1% ~ 5% (4-wire type) 10m Ω			
	Capacitance	1pF~40mF±2%~5%			
	Inductance	1uH~50H±2%~5%			
	Diode	0.1~9V±1%~3%			
	Zener diode	0.1~15V±1%~3%			
	Transistor	Vce saturation voltage and B value are tested in three stages			
	field-effect transistor	Vds.Cds&Rd (on)			
	Optical coupler and power failure organ	The four terminals test their on-voltage or resistance			
Impedance series-parallel connection		Using multi-frequency test and phase separation method (8 ~82 degrees)			
Polarity of Electrolytic Capacitor and Leakage Detection Technology		Three-terminal characteristic discrimination comparison technology test and two-terminal leakage current test			
Feature selection function	Microresistor (4-wire Type)	Minimum 50mQ (4-wire) ± 10mΩ			

	DRMode	Detectable diode or IC forward parallel leakage and empty soldering
	Key press function	During the key test, the program is suspended, and manual operation is used to detect whether the key is good or bad
	Automatic stamp	Test PASS or Fail automatic stamping equipment
	Monitor placement	Monitoring placement orientation (PASS or Fail shunt)
	Multi-pressure bed	Multi-or double-press system
	First pass yield surveillance	When the first pass yield < X% is the alarm bell and < Y% is the alarm bell, stop the line inspection
	Graphic function	Board View)& Automatic Production of Board View
	Connecting board function	The linking board is automatically unfolded & the sub-board is selected and tested & the linking board is automatically detected
	Test data archiving	Save the test data selection items & barcode scanning
	SFIS	ShineWave SFIS、Sajet SFIS、Off-Line SFIS
Hardware protection function	Infrared protection function	
Operating system	Windows XP	
Computer configuration	CPU: Dual Core 2.8+Memory: 2GB/ Hard Disk: 1000G+G/Monitor: 19"Monitor	
Printer	Epson Bill printer	
Machine size (length * width * height)	1000mm*750mm*1600MM (Machine size can be customized according to customer requirements)	
Circuit board dimensions	500mmX350mm	
Air pressure demand	3~6Kgs/cm2	
Working Environment-Power Supply	Temperature 0-45 degrees, humidity 10-90%&AC220V	